ISSCC2015 REPORT

ISSCC2015 in San Francisco, California, USA

Feb.22-26, 2015

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From February 22 to February 26, 2015, one of the biggest and prestigious conference related to solid-state circuit design which is known as The International of Solid-State Circuits Conference for year of 2015 (ISSCC2015) has been successfully held at San Francisco Marriot Marquis Hotel, Francisco, California. This conference has been organized by IEEE of Solid-State Circuits Society (SSCS). This year, I have been given an opportunity to attend this conference. With almost nine to twelve hours away by direct flight from Narita International Airport (NRT) to San Francisco International Airport (SFO) by of United Airline. This is my first experience to be in the Americas especially United State of America with effort to expose myself with the new innovations and frontier technologies in the global world.

In this ISSCC2015, approximately more than 3000 attendees were participated during this conference from around the world. In addition, the participation from many leading and competitive companies in the world, made this conference more attractive.

Due to my current research related to mixed signals, this conference was very suited



San Francisco Airport (SFO).



In front gate to Bay area Rapid Transit



Reached at Powell Station from SFO by Bay

Area Rapid Transit (BART).

and provided more widen view for my future work. By attending this conference, gave me more updated information. Therefore, it was very worth experience.

There are a lot of sessions provided which very benefit to participants. On the first day February 22, 2015, I was attending the tutorial related to my current research, high-speed current-steering digital-to-analog converter (DAC) designs which conducted by the expert. With one and a half hour, I have been exposed to the more details both theory and implementation. Then in the evening, there is a session for the selected students from over the world to present their current research by one minute short presentation and poster.

On the second day, February 23, 2015, beginning with plenary session which talk given by speakers from academia and industry. The oral sessions begin after afternoon and have been organized in parallel depend on to the topics. I attended the session related to analog technique. After that, there is demonstration session for the selected paper. From 8 pm to 10 pm, there is discussion session with very experienced panelists.

On the third day, February 24, 2015, the oral sessions have started from the morning. That day, I attended session related high performance wireless and data converter techniques. Same as previous day, there is also demonstration session from 5 pm to 7 pm. At the night, I attended the second discussion related to the innovating on the tapeout treadmill.



Conference location.



ISSCC2015.



Plenary session.



Picture taken with one of the staff at ISSCC2015.

Fourth day, February 25, 2015, I attended the oral session related to energy harvesting and switched capacitor power conversion in the morning session and Nyquist-rate converters in the afternoon session.

And on the last day of conference, I attended the short course related to circuit design in advanced CMOS technologies especially to design with lower supply voltage. It was one whole day course divided into four speakers by their expertise: digital circuit design, mixed signal design, analog-to digital converter (ADC) and radio frequency (RF) design.

Even the schedule was quite packed, I managed to take some space to enjoy the San Francisco beautiful sceneries. I have been time to get at ferry building which built in 1915. There are a lots of nice building along the Jefferson Street and the famous Fisherman's Wharf which very well-known with their fresh seafood. There are another places such as Union square, Mascone Center, many others.

As my personal opinion, even though I did not presenting any paper in ISSCC2015, but by attending this conference, it gave me motivation to work more and hopefully I can manage to be one of the presenters in this future conference. Besides that, I also gained my knowledge about the America peoples in terms of social and culture. Not forget about their foods and many interested things.

Finally, I would like to thank Professor Dr Haruo Kobayashi, Mr Nobuyoshi Ishikawa, Gunma University and Semiconductor Technology Academic Research Center (STARC) for their kindly support in this visit.



Yerba Buena Garden near Metron



Ferry port built in 1915.



Aquarium of the Bay.



Fisherman's Wharf.